

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: Barry James Lytollis

Serial No.: 09/894, 099

Group Art Unit: 2836

Filing Date: June 28, 2001

Examiner: Not yet assigned.

For: Protection of Intrinsically Safe Circuits

DATE OF DEPOSIT: November 5, 2001

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TYPED NAME: Steven J. Rocci REGISTRATION NO. 30,489

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Assistant Commissioner for Patents Washington DC 20231			
Sir:	TRANSMITTAL LETTER	CENTER 2800	2002
	Transmitted herewith for filing in the above-identified patent application	is:	
	A Preliminary Amendment.		
	An Amendment Responsive to the Office Action Dated		·
	An Amendment Supplemental to the Paper filed		<u></u> .
	Other:		_ •
	Applicant(s) has previously claimed small entity status under 37 CFR §1.		
	Applicant(s) by its/their undersigned attorney, claims small entity status to CFR §1.27 as:	unde	r 37

DOCI	KET NO.: THOM-0015 - 2 -	PATENT			
	an Independent Inventor a Small Business Concern				
	a Nonprofit Organization				
	This application is no longer entitled to small entity status. It is requested that this be noted in the files of the Patent and Trademark Office.				
	Substitute Pages of the Specification are enclosed.				
	An Abstract is enclosed.				
	Sheets of Proposed Corrected Drawings are enclosed.				
\boxtimes	A Certified Copy of each of the following applications: Certified Copy of priority document no. GB0016524.1 filed 07/06/00 in the United Kingdom is enclosed.				
	An Associate Power of Attorney is enclosed.				
	Information Disclosure Statement.				
	Attached Form 1449.				
	A copy of each reference as listed on the attached Form PTO-1449 herewith.	is enclosed			
	Appended Material as follows:	·			
	Other Material as follows:	·			
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- 3 -

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DOCKET NO.: THOM-0015

				SMALL	ENTITY	NOT SMA	LL ENTITY
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TOTAL CLAIMS	13	20 (20 MINIMUM)	0	\$9 EACH	\$	\$18 EACH	\$0
INDEP. CLAIMS	3	3 (3 MINIMUM)	0	\$42 EACH	\$	\$84 EACH	\$0
FIRST PRESENTATION OF MULTIPLE DEPENDENT			\$140	\$	\$280	\$0	
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Petition is hereby made under 37 C.F.R. 1.136(a) to extend the time for response to the Office Action of @@ to and through @@ comprising an extension of the shortened statutory period of @@ month(s).
The Commissioner is hereby requested to grant an extension of time for the appropriate length of time, should one be necessary, in connection with this filing or any future filing submitted to the U.S. Patent and Trademark Office in the above-identified application during the pendency of this application. The Commissioner is further authorized to charge any fees related to any such extension of time to deposit account 23-3050. This sheet is provided in duplicate.

The Commissioner is authorized to charge payment of the following fees and to refund any overpayment associated with this communication or during the pendency of this application to deposit account 23-3050. This sheet is provided in duplicate.				
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SHOULD ANY DEFICIENCIES APPEAR with respect to this application, including deficiencies in payment of fees, missing parts of the application or otherwise, the United States Patent and Trademark Office is respectfully requested to promptly notify the undersigned.

Date: November 5, 2001

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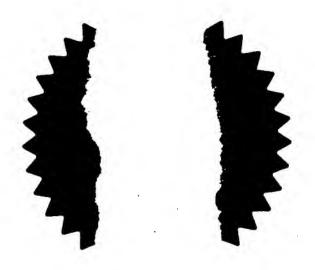
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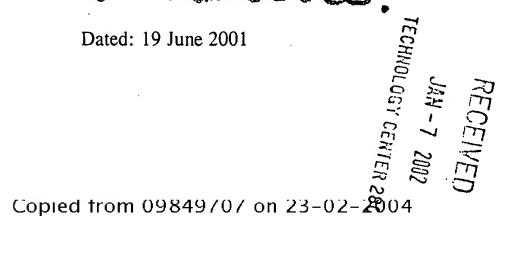
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Continuation sheets of this form

Description

6

Claims

Abstract

Drawing(s) 2

If you are also filing any of the following, state how many against each item.

Priority documents

Translations of priority documents

Statement of inventorship and right to grant of a patent (Patents Form 7/77)

Request for preliminary examination and search (Patents Form 9/77)

Request for substantive examination (Patents Form 10/77)

Any other documents (Please specify)

11.

10.

We request the grant of a patent on the basis of this application

Signature W.P. Korfm & Co.

te July 5, 2000

W. P. Thompson & Co.

12. Name and daytime telephone number of person to contact in the United Kingdom

Roger B Thomson 01462 682139

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PROTECTION OF INCENDIVE CIRCUITS

This invention relates to the protection of potentially incendive circuits. It is particularly concerned with the 5 protection of circuits which operate within hazardous areas, for example in the presence of flammable gases.

In systems where power supplies feed a number functional modules, where there may be the presence of flammable gases, there is a need to ensure that the system is 10 safe. This means that the wiring between the power supplies. and the modules must be protected in some way so that it is not incendive, even when the modules are unplugged with the system running. One way to achieve this is to use Exi protection, with the power supplies having electronic power 15 limiters in each output. These power limiters define the maximum output voltage and limit the short-circuit current. However, these limiters are quite complex because they must meet the conflicting requirements of being precise yet operate This complexity is duplicated to meet the fault-count 20 requirements for Exib.

It is an object of the present invention to provide a means of protecting potentially incendive circuits using a simpler system than the relatively complex, conventional powerlimiters.

It is a further object of the invention to provide means whereby one affords complete protection to the wiring and to any plugs and sockets along the way. Desirably, provided that the inductance and capacitance limits are not exceeded, the wiring from the power supply to the modules is protected from open-circuit and short-circuit faults, so that no special segregation is required.

Since the wiring is wholly contained within the 1/1 node, the segregation between conductors can be controlled in the construction of the circuitry. If this segregation is made infallible, then short-circuit faults need not be considered and one only needs to protect against series breaks. For this, simpler protection schemes are adequate.

If one is concerned only with series breaks, then this removes the constraint to put the electronic protection upstream from the wiring to be protected. It simply has to be somewhere in series with the circuit. The importance of this to achieving protection in accordance with the invention will be apparent from the description given hereinafter.

In order that the invention may be more fully understood, a transistor switch system in accordance with the invention will now be described by way of example and with reference to the accompanying drawings. In the drawings:

- Fig. 1 shows a known example of the use of an active voltage and current limiter to protect against wiring shorts and breaks;
- 20 Fig. 2 illustrates the concept underlying the present invention, in which the voltage and current which is sensed upon a break in the wiring opens a switch;
 - Fig. 3 is a circuit diagram showing a more practical arrangement in accordance with the invention;
- 25 Fig. 4 shows a transistor switch circuit which can be used in accordance with the invention; and
 - Fig. 5 shows a circuit in accordance with the invention for testing purposes.

The circuit shown in Fig. 1, to illustrate the prior art, 30 has a voltage supply 10, a load 12 and an active voltage and current limiter 14 upstream from the load. A pin 16

illustrates a short-circuit condition and a break in the wiring is indicated at 18. The active limiter 14 shown in Fig. 1 protects by limiting the voltage and current which is available to the circuit below a known incendive limit. 5 However, this is not strictly necessary. What is required is to restrict the voltage and current which is available to a developing spark to levels below the incendive limit.

Fig. 2 illustrates this concept. Al is a voltage sensor, sensing the voltage developed across the break 18 in the 10 circuit. A2 is a current sensor, sensing the current flowing through it. The two sensors A1 and A2 are combined in a manner which will enable a switch 20 to open before the V/I characteristic exceeds the incendive limit. It is to be noted that the power available to the load 12 is not now constrained 15 to be below the incendive limit.

It is also to be noted that the circuit shown in Fig. 2 merely illustrates the concept underlying the present invention. In particular, only the wiring between the inputs of voltage sensor Al is protected.

20 Fig. 3 shows a more practical arrangement in which all of the circuit to the left-hand side of the voltage sensor A1 is now protected. Voltage sensor A1 now senses the voltage at the load end of the circuit, protecting all the wiring to its left. The voltage supply 10 is now added to the voltage 25 sensed by voltage sensor A1, but is constant and allowance can be made for it. More importantly, the current sensor A2 is omitted and the output of the voltage sensor A1 is taken directly to the switch 20. It is known that for hydrogen, the most incendive gas group, it is impossible to get ignition 30 with a voltage which is less than about 8 volts at any current, provided that the current is insufficient to cause

hot or molten metallic particles to be thrown off from the sparking contact. If the voltage which is allowed to develop across a breaking contact is restricted to less than 8 volts, then a precise current limit is not required. In some 5 apparatus, it may be possible to rely on the fature of the load 12 to determine the maximum current.

The circuit shown in Fig. 3 will only be effective if the voltage sensor A1 and the switch 20 are sufficiently fast. Experience in the use of active limiters suggests that the 10 protection must operate within a few microseconds. Research has been reported which suggests that the minimum spark duration capable of causing ignition is around $8\mu s$. A transistor operating in a common-base configuration can be much faster than this and can be configured in a simple 15 circuit which combines both the sensing and switching functions. Fig. 4 shows this in outline.

In Fig. 4 which shows a common-base transistor switch 20, a zener diode Z1 is connected to the base of the transistor. The voltage of zener diode Z1 is selected so that, when the 20 circuit is unbroken, the supply voltage is present at the emitter of the switch 20 and base current is drawn through the zener diode Z1. The transistor switch 20 is turned hard on and current is fed to the load 12. If a break occurs, as indicated at 18, voltage is dropped across the break as a 25 spark develops, resulting in the emitter voltage of the transistor 20 dropping. At the point when the emitter voltage drops below the zener voltage, plus the emitter-base drop, the transistor 20 will turn off and break the circuit.

Fig. 5 shows a circuit which embodies these principles.

30 A series diode 22 is connected between the emitter of transistor 20 and the voltage supply 10. A resistance R1 is

connected between the base of the transistor 20 and the zener diode Z1. A second resistance R2 is connected between the emitter and base of the transistor 20. The resistance R1 limits the base current through transistor 20 to about 15 mA.

In one practical test circuit to this design, a load of 26 ohms was used, giving a load current of about 850mA, which is normally incendive in a constant current circuit down to around 12 volts or so.

The circuit was spark ignition tested according to EN 50020, using the 21% hydrogen in air explosive test mixture specified for group IIC gases. During this test, the power supply voltage was held constant at 24V, while the voltage of the zener diode Z1 was progressively reduced until ignition occurred. At the same time, resistor R1 was adjusted to 15 maintain the current through zener diode Z1 to about 15mA. The effect of reducing the zener diode voltage in this way was to increase the voltage across the spark before transistor 20 turns off.

There were no ignitions until the spark voltage exceeded 20 about 12V, demonstrating that the circuit does provide the expected protection.

In another test the spark V/I characteristics were monitored using a storage oscilloscope and a current probe. Many sparking events were monitored and none showed voltage 25 and current simultaneously present at the spark, at a resolution down to below 1μ s. This demonstrates the inherent speed of the common-base circuit.

This protective circuit in accordance with the invention is very simple, inherently fast, and can be easily cascaded to meet Exib fault-count requirements.

In a practical embodiment, the transistor 20 would be

protected against the over-dissipation which would occur if the input voltage happened to be just enough to bias it into its linear region. This can be achieved by adding a simple current limit, or by designing hysteresis into the switching action.

In a practical embodiment it would also be necessary to limit the current, to limit the inductive energy in the wiring. If the load circuitry will not suffice, then a simple current limit may be necessary, or, if the volt-drop can be tolerated, an infallible series resistor may be sufficient. It would also be necessary to include a fuse to protect against sustained overcurrent faults, which would cause thermal ratings to be exceeded.

